

بِسْمِ اللَّهِ الرَّحْمَنِ الرَّحِيمِ

Republic of Iraq

The Ministry Of Higher Education
& Scientific Research



University: Baghdad University
College: College of Science for women
Department: Computer Dept.
Stage: Second Stage/ Second semester
Lecturer name: Raja 'a Mureeh
Mohammed

Qualification: Master in Computer
Science

Place of work: College of Science for
women/ Computer Dept.

Syllabus Form

Instructor Name	Raja 'a Mureeh Mohammed				
E-mail	Rajaa007700@yahoo.com				
Course Title	Computer Architecture 2				
Course Coordinator	111CS213				
Course Objectives	This article aims to enable the student to know how numbers representation in computer, to identify the method of how CPU communication with memory addresses in the computer's memory, to identify the method of how devices transfer data to CPU, and get knowledge of the different interrupts types.				
Course Description	<ol style="list-style-type: none">1. Getting the knowledge of the formatting floating point.2. Understand the assembly instruction and how to program the Intel processor, the other parts related to number representation.3. Knowledge how devices transfer data with the microprocessor.4. Getting the knowledge about Intel 80836 microprocessor				
Textbook	The 8086 and 80888 microprocessor, Avtar singh, 4'th Edition, 2003.				
References	<ol style="list-style-type: none">1- The Intel microprocessor architecture programming and interfacing, Barry B. Brey, 6'th Edition ,2003.2- Advance computer Architecture, Peter Lascsuk, 1998.				
Course Assessments	Term Tests	Laboratory	Quizzes	Project	Final Exam
	20%	15%	5%	-	60%
General Notes					

Course Weekly Outline

Week	Date	Topes Covered	Lab. Experiment Assignments	Notes
1		floating point formatting	Reading and writing character in assembly language	
2		programming input-output	practice	
3		programming input-output	practice	
4		Interrupts types	practice	
5		Real and protected mode interrupts	practice	
6		DMA	Reading and writing string in assembly language	
7		pin assignment of DMA	practice	
8		Hardware organization of the memory address space	practice	
9		Hardware organization of the memory address space	practice	
10		Exam	practice	
11		Device identification	Exam	
12		Device identification	Logical instruction	
13		Intel 80836 microprocessor	practice	
14		Intel 808386 microprocessor	practice	
15		Intel 808386 microprocessor	practice	
16		Exam	Exam	

Instructor Signature:

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