## Republic of Iraq The Ministry Of Higher Education & Scientific Research

بسم الله الرحمن الرحيم



University: Baghdad University College: College of Science for women Department: Computer Dept. Stage: Second Stage/ First Semester Lecturer name: Raja 'a Mureeh

Mohammed

**Qualification: Master in Computer** 

**Science** 

Place of work: College of Science for

women/ Computer Dept.

## Syllabus Form

Instructor Name	Raja 'a Mureeh Mohammed						
E-mail	Rajaa007700@yahoo.com						
Course Title	Computer Architecture 1						
Course Coordinator	111CS206						
Course Objectives	This article aims to enable the student to identify the internal structure of the microprocessor, and to identify the method of accomplish CPU functions and how communication with memory addresses in the computer's memory, and get knowledge of the different CPU versions.						
Course Description	<ol> <li>Getting the knowledge of the CPU internal structure.</li> <li>Understand the assembly instruction and how to program the Intel processor, the other parts related to number representation.</li> <li>Knowledge of other devices connected with the microprocessor.</li> </ol>						
Textbook	The 8086 and 80888 microprocessor, Avtar singh, 4'th Edition, 2003.						
References	<ul><li>1- The Intel microprocessor architecture programming and interfacing, Barry B. Brey, 6'th Edition ,2003.</li><li>2- Advance computer Architecture, Peter Lascsuk, 1998.</li></ul>						
Course Assessments	Term Tests 20%	Laboratory 15%	Quizzes 5%	Project	Final Exam 60%		
General Notes							

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## ourse Weekly Outline

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≤ Date		Topes Covered	Lab. Experiment	Notes
Week			Assignments	
1		Introduction to microcomputers	Introduction to assembly	
		and microprocessor	language	
2		General CPU Architecture	Move instruction	
3		Intel 8085 microprocessor	Arithmetic instruction (add)	
4		Intel 8086 microprocessor	Arithmetic instruction (sub)	
5		Register organization of 8086	practice	
6		8086 instruction set	Arithmetic instruction (mul)	
7		8086 instruction set	Arithmetic instruction (div)	
8		Addressing modes of 8086	practice	
9		Addressing modes of 8086	Reading and writing in assembly language	
10		Instruction cycle in 8086	practice	
11		8086 pin assignment	Control instruction	
12		8086 pin assignment	Control instruction	
13		8086 pin assignment	practice	
14		Instruction format in 8086	practice	
15		Instruction format in 8086	practice	
16		Exam	Exam	

Instructor Signature:

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